

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Frank O'Mahony et al.

TMAR: 3 1 2005  
**LOW LOSS INTERCONNECT STRUCTURE FOR USE IN MICROELECTRONIC  
CIRCUITS**

DOCKET NO.: 884.405US1

Serial No.: 09/893,023

Filed: June 27, 2001

Due Date: February 3, 2005

Examiner: Ahmed Sefer

Group Art Unit: 2826

**MS AF**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

A return postcard.  
 An Amendment and Response under 37 CFR 1.116 (9 Pages).

If an additional fee is required due to changes to the claims, the fee has been calculated as follows:

CLAIMS AS AMENDED						
	(1) Claims Remaining After Amendment		(2) Highest Number Previously Paid For	(3) Present Extra	Rate	Fee
<b>TOTAL CLAIMS</b>	<b>28</b>	-	<b>28</b>	<b>0</b>	<b>x 50.00 =</b>	<b>\$0.00</b>
<b>INDEPENDENT CLAIMS</b>	<b>4</b>	-	<b>4</b>	<b>0</b>	<b>x 200.00 =</b>	<b>\$0.00</b>
<b> )MULTIPLE DEPENDENT CLAIMS PRESENTED</b>						<b>\$0.00</b>
<b>TOTAL</b>						<b>\$0.00</b>

Please consider this a **PETITION FOR EXTENSION OF TIME** for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
Customer Number 21186

By: David R. Cochran  
Atty: David R. Cochran  
Reg. No. 46,632

**CERTIFICATE UNDER 37 CFR 1.8:** The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 26th day of January, 2005.

Name

Amy Moriarty

Signature

Amy Moriarty

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
(GENERAL)



**EXPEDITED PROCEDURE – EXAMINING GROUP 2826**

S/N 09/893,023

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Frank O'Mahony et al.	Examiner:	Ahmed Sefer
Serial No.:	09/893,023	Group Art Unit:	2826
Filed:	June 27, 2001	Docket No.:	884.405US1
Title:	LOW LOSS INTERCONNECT STRUCTURE FOR USE IN MICROELECTRONIC CIRCUITS		
Assignee:	Intel Corporation	Customer No.:	21186

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**AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116**

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In response to the Final Office Action mailed December 3, 2004, please amend the application as follows: